

HRV103B

Silicon Schottky Barrier Diode for Rectifying

REJ03G0399-0300 Rev.3.00 Mar 25, 2008

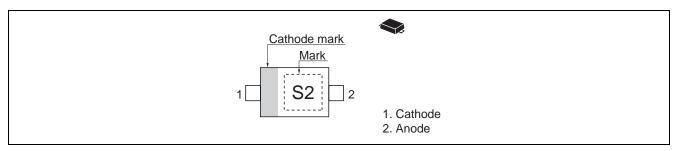
Features

- Low reverse current and suitable for high efficiency rectifying.
- Thin Ultra small Resin Package (TURP) is suitable for high density surface mounting and high speed assembly.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code	
HRV103B	S2	TURP	PUSF0002ZC-A	

Pin Arrangement



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V _{RRM}	30	V
Reverse voltage	V _R	30	V
Average rectified current	l ₀ * ²	1	А
Non-Repetitive peak forward surge current	I _{FSM} * ¹	5	А
Junction temperature	Тј	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. 10 ms sine wave 1 pulse

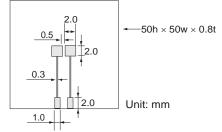
2. Ta = 48°C, With Ceramics board (board size: 50 mm \times 50 mm, Land size 2 mm \times 2 mm) Short form wave (θ 180°C), V_R = 15 V.

Electrical Characteristics

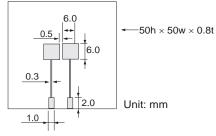
 $(Ta = 25^{\circ}C)$

ltem	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _{F1}		—	0.35	V	I _F = 100 mA
	V _{F2}	_	—	0.45		I _F = 700 mA
	V _{F3}	_	—	0.50		I _F = 1 A
Reverse current	I _{R1}	_	—	10	μΑ	V _R = 5 V
	I _{R2}	_	—	100		V _R = 30 V
Capacitance	С	_	—	40	pF	V _R = 10 V, f = 1 MHz
Thermal resistance	Rth(j-a)	_	100	—	°C/W	Ceramics board *1
		_	200	—		Glass epoxy board *2

Notes: 1. Ceramics board

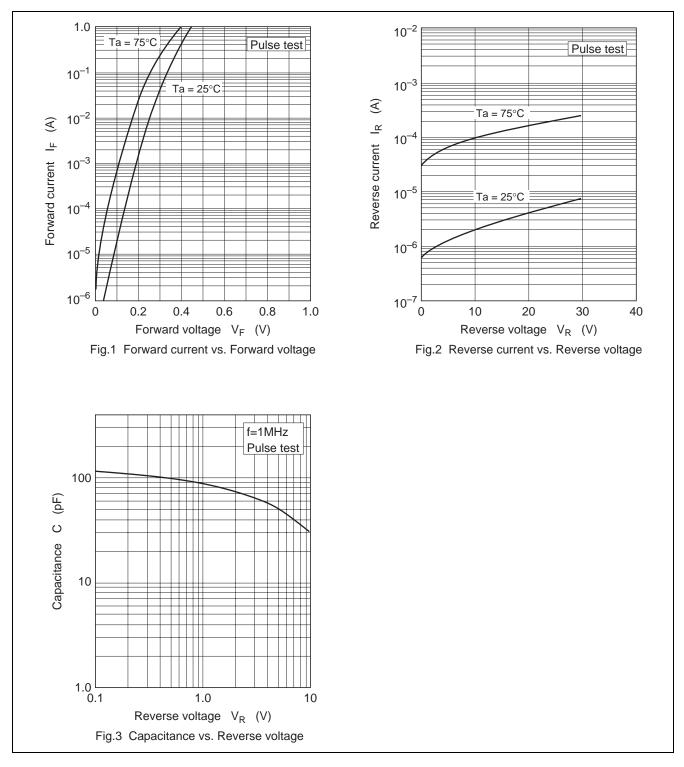


2. Glass epoxy board



3. TURP is the structure which radiates heat to a substrate, please perform mounting to a substrate by reflow.

Main Characteristics



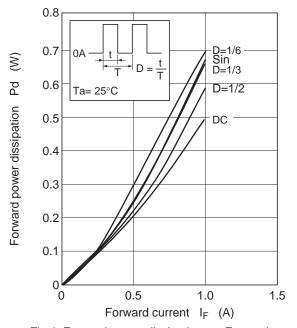


Fig.4 Forward power dissipation vs. Forward current

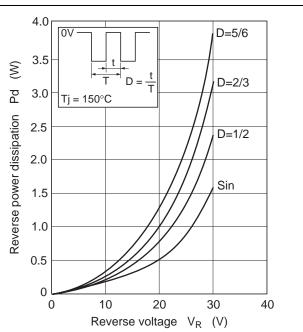
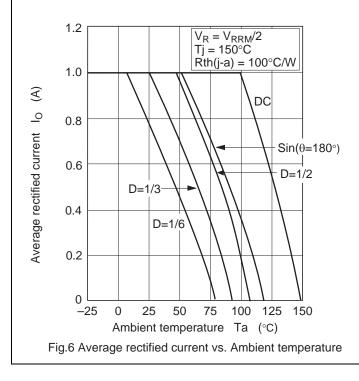
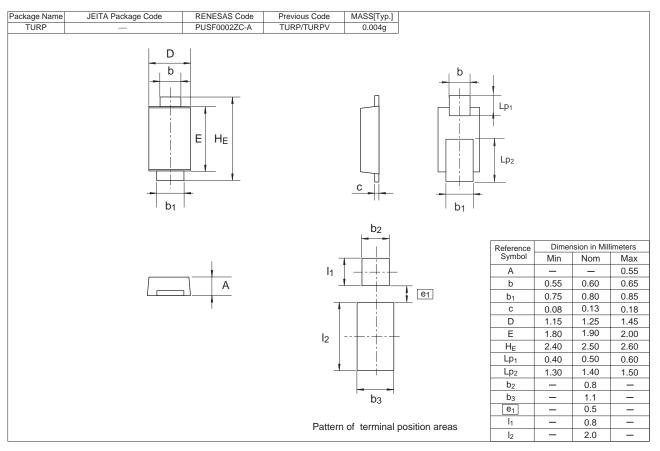


Fig.5 Reverse power dissipation vs. Reverse voltage



Package Dimensions



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Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510

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